

US009680097B2

(12) United States Patent

Mandal et al.

(10) Patent No.: US 9,680,097 B2

(45) **Date of Patent: Jun. 13, 2017**

(54) ORGANIC THIN FILM TRANSISTORS AND METHODS FOR THEIR MANUFACTURING AND USE

(71) Applicant: INDIAN INSTITUTE OF

TECHNOLOGY KANPUR, Kanpur

(IN)

(72) Inventors: Saumen Mandal, Naskarpur (IN);

Monica Katiyar, Kanpur (IN)

(73) Assignee: INDIAN INSTITUTE OF

TECHNOLOGY KANPUR (IN)

*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 0 days.

(21) Appl. No.: 14/782,600

(22) PCT Filed: Apr. 3, 2014

(86) PCT No.: **PCT/IB2014/060398**

§ 371 (c)(1),

(2) Date: Oct. 5, 2015

(87) PCT Pub. No.: WO2014/162287

PCT Pub. Date: Oct. 9, 2014

(65) Prior Publication Data

US 2016/0043315 A1 Feb. 11, 2016

(30) Foreign Application Priority Data

Apr. 6, 2013 (IN) 1032/DEL/2013

(51) **Int. Cl.**

 H01L 51/00
 (2006.01)

 H01L 51/05
 (2006.01)

 H01L 27/32
 (2006.01)

(52) U.S. Cl.

CPC *H01L 51/0005* (2013.01); *H01L 51/0022* (2013.01); *H01L 51/0096* (2013.01);

(Continued)

(58) Field of Classification Search

CPC H01L 51/0005; H01L 51/0022; H01L 51/0096; H01L 51/0097; H01L 51/052; (Continued)

(56) References Cited

U.S. PATENT DOCUMENTS

7,098,061 B2 8/2006 Sirringhaus et al. 7,351,606 B2 4/2008 Arias (Continued)

FOREIGN PATENT DOCUMENTS

CN 1545453 A 11/2004 CN 101356651 A 1/2009

OTHER PUBLICATIONS

"Printed and Flexible Electronics Services: Development and optimization of materials and devices," PARC, accessed at URLhttp://www.parc.com/publication/2355/printed-and-flexible-electronics-services.html on Oct. 7, 2015, pp. 2.

(Continued)

Primary Examiner — Karen Kusumakar (74) Attorney, Agent, or Firm — Ren-Sheng International

(57) ABSTRACT

Methods of forming an organic thin film transistor are provided. The methods include providing a substrate and depositing and patterning a gate electrode on a first surface of the substrate. The methods include dispensing a first droplet of an insulating material on the gate electrode on the substrate and dispensing a second droplet of a semiconductor material on a first surface of the first droplet. The second droplet forms a hydrophobic structure having a central cavity. The methods also include dispensing a third droplet of a conductor material on a first surface of the second droplet such that the conductor material substantially fills the central cavity of the hydrophobic structure and forms a conductor material layer around the central cavity to define a source electrode and a drain electrode of the organic thin film transistor.

25 Claims, 5 Drawing Sheets



